

HP Integrity Server HP Technical Update Days 26./27.9.2005

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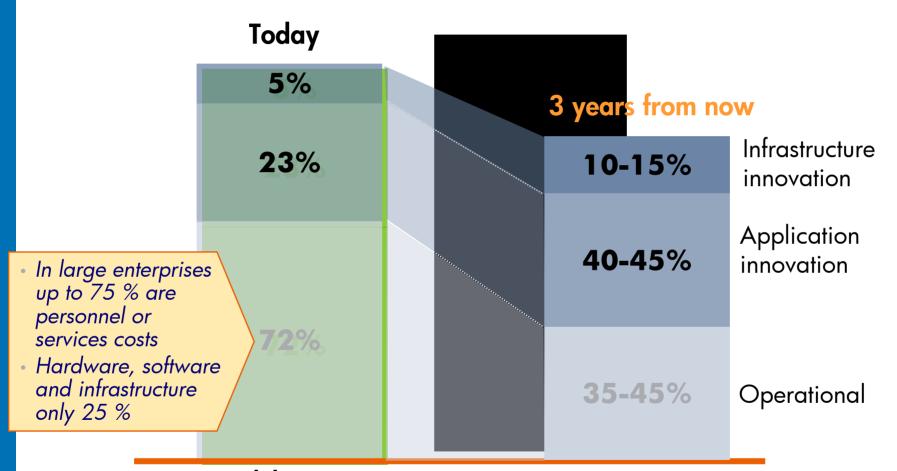
hp

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Reduce the cost AND increase the value of IT





How can we move to a sustainable cost structure?



The Adaptive Enterprise

Business and IT synchronized to capitalize on change



Delivers: Simplicity, Agility, Value

Standardization

Leverage common components and processes to reduce cost, simplify change

Virtualization

Utilization is optimized and IT supply meets business demand

Management

Automate the dynamic link between business and IT

HP Server und Storage Portfolio Das breiteste Angebot für Enterprise-Anwendungen



Optical

family

ESL series

MSL series

Tape Autoloader

series

Tape drive

families

Options

Media

Software

SAN Infrastructure

StorageWorks

CASA

XP series

FVA series

VA series

MSA family

NAS family





Application



Database



Multi-OS



HP-WX11i





NonStop OS

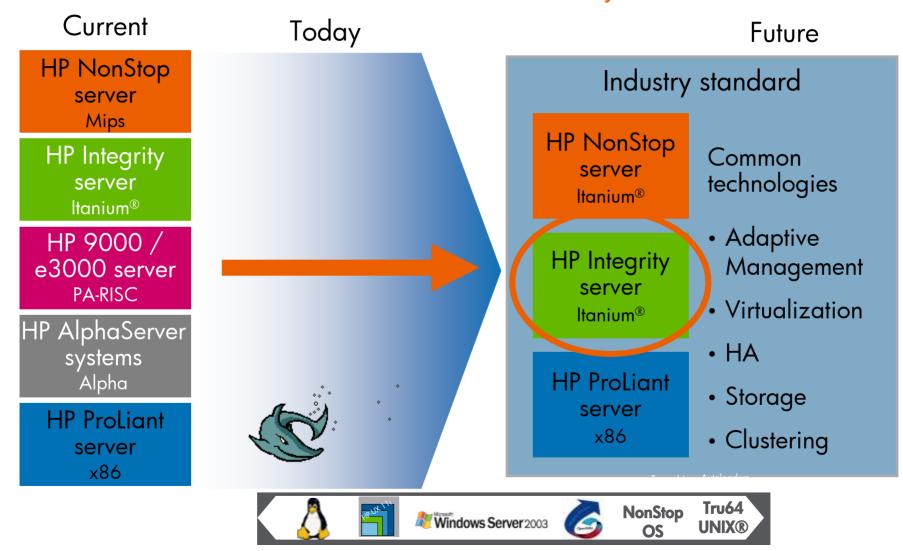
Tru64

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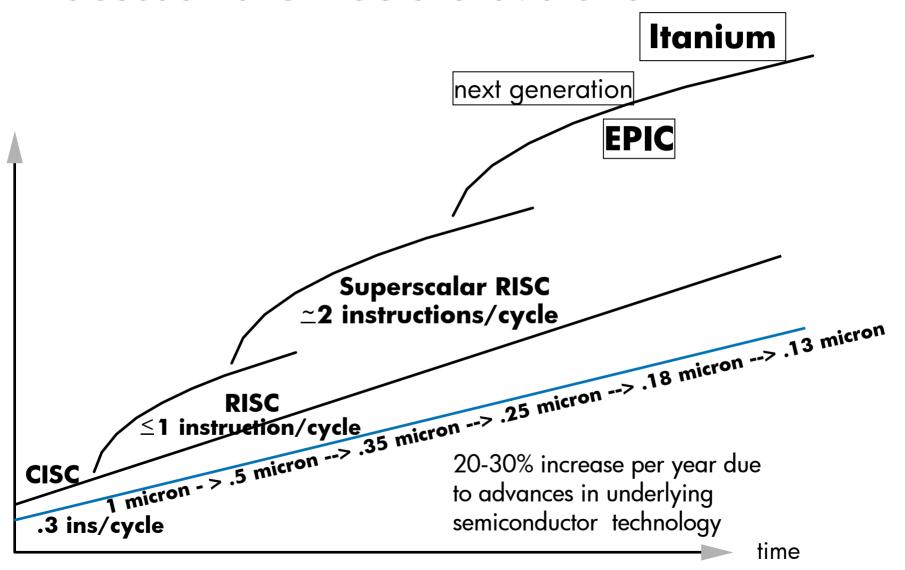
BCS Migration Scenario based on Standards

Moving to 3 leadership product lines – built on 2 industry standard architectures



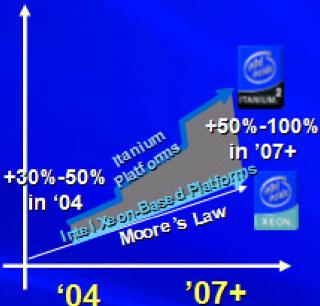


Processor architecture evolution



Itanium® 2 & Xeon™ Processor MP Comparison

Performance



*For Enterprise & Technical Computing Application Segments

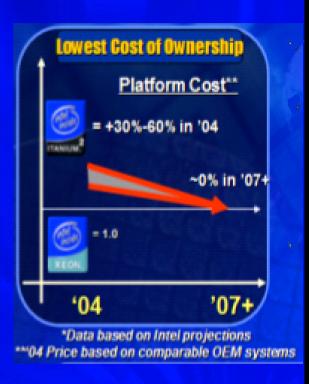
Today: 30-50% higher performance

2007+: Up to 2x performance at same system price

Reliability

Characteristic	Itanium® 2	Xeon™ MP
Error recovery on data bus-ECC	1	
Internal soft error logic check	2005	
Machine Check Architecture	✓	
Bad data containment	1	
Cache Reliability	2005	
Lockstep support	1	1
Memory SDEC, retry double-bit	1	1
Memory spares	1	1
Partitioning	√node	√ node

Price

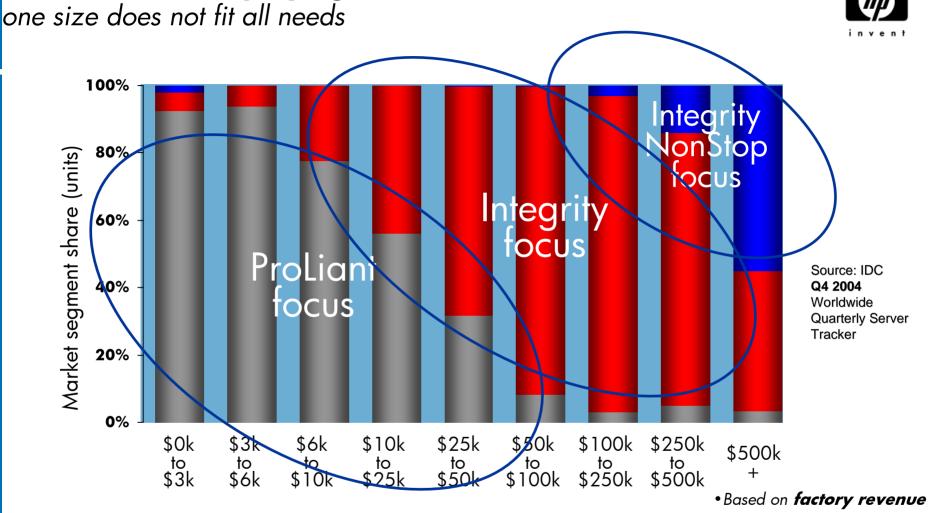


High-end "RISC"-level RAS

Itanium's EPIC Architecture: Highest Performance, Reliability, Scalability



Customers are deploying different classes of servers



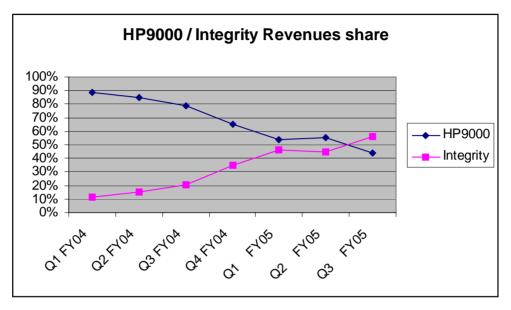
X86: \$21.8bn market (for cy2004)

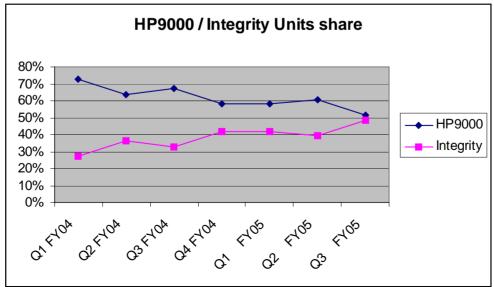
RISC/EPIC: \$19.8bn market (for cy2004) CISC: \$7.5bn market (for cy2004)

HP Restricted – For HP and Partner Internal Use Only









Intel® Itanium® Processor Family Roadmap

2004

2005

2006

2007

Future

Leading Performance

4S+

Itanium® 2
Processor (Madison 9M)

1.6 GHz, 9M

Montecito

Dual Core, 24MB Multi-threading **Montvale**

Dual Core, Multi-threading **Tukwila**

Multi-core

Poulson

Leading \$/FLOPS

28

Itanium® 2
Processor (Fanwood)
1.6 GHz, 3M, DP

Millington

DP, Montecito-based

DP Montvale

DP, Montvale-based

Dimona

DP, Tukwila-based

Future

DP, Poulson-based

Lower Power

25

LV Itanium® 2
Processor (LV Fanwood)
1.3 GHz, 3M, DP

LV Millington

DP, Low Voltage, Montecito-based **LV Montvale**

DP, Low Voltage, Montyale-based **LV Dimona**

DP, Low Voltage, Tukwila-based **Future**

DP, Low Voltage, Poulson-based

New Technologies

- Multi-core
- Multi-threading
- Dynamic performance boost (Foxton)
- Dynamic power management (DBS)
- Cache reliability (Pellston)
- Intel® Virtualization Technology

- Multi-core enhancements
- Enhanced RAS
- Enhanced virtualization
- Enhanced I/O & memory
- Common system architecture w/ Intel® Xeon™



All products, dates, comparisons, and information are preliminary and subject to change without notice.

Long Term Goal: 1M Transactions per Minute

Today

In 2007







With planned performance improvements, a 4-way Itanium®-based server in '07 could deliver equivalent OLTP of a current 64-way system, delivering dramatically

- Lower TCO
- Lower power consumption
 - Higher density



Shown are representations of 64-way system (today) and 4-way system (2007). Not to scale.



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HP Virtual Server Environment



(Global) Workload Manager (WLM & gWLM) and Single Virtual View (SVV)

processor sets resource partitions hard partitions hard partitions virtual partitions within partitions with PRM with multiple nodes with a node within a hard partition Virtual **Processor Clusters nPartitions Partitions** Sets **Resource Partitions Availability** Virtualization Security **Partitioning** - a group of electrical isolation processors - a scheduling complete domain hardware and dynamic CPU software isolation assignment - complete software cell granularity CPU granularity complete - multiple OS isolation dynamic CPU, single OS image hardware and dynamic CPU memory & I/O images - HP-UX, Windows, - ownership & software isolation migration allocation CPU granularity node granularity Linux, and/or access share (%) permissions multiple OS OpenVMS in multiple HP-UX OS granularity - process binding different partitions single OS image images images

Capacity Management (iCAP, TiCAP & PPU)



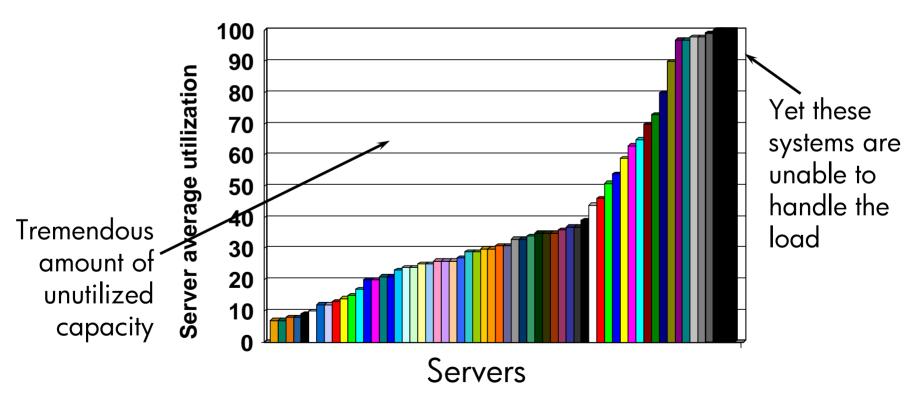
isolation



Challenge

Enterprises have unused server capacity yet still can't meet demand

Utilization of HP-UX servers at an HP customer



Most reports put average utilization at approx 25% - 30% for Unix servers





